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## FORM A2-3 Compliance Verification Report for Type A Inverter Connected Power Generating Modules

This form should be used by the **Manufacturer** to demonstrate and declare compliance with the requirements of EREC G99. The form can be used in a variety of ways as detailed below:

#### 1. To obtain Fully Type Tested status

The **Manufacturer** can use this form to obtain **Fully Type Tested** status for a **Power Generating Module** by registering this completed form with the Energy Networks Association (ENA) Type Test Verification Report Register.

#### 2. To obtain Type Tested status for a product

This form can be used by the **Manufacturer** to obtain **Type Tested** status for a product which is used in a **Power Generating Module** by registering this form with the relevant parts completed with the Energy Networks Association (ENA) Type Test Verification Report Register. Where the **Manufacturer** is seeking to obtain **Type Tested** status for an **Interface Protection** device the appropriate section of Form A2-4 should be used.

#### 3. One-off Installation

This form can be used by the **Manufacturer** or **Installer** to confirm that the **Power Generating Module** has been tested to satisfy all or part of the requirements of this EREC G99. This form shall be submitted to the **DNO** as part of the application.

A combination of (2) and (3) can be used as required, together with Form A2-4 where compliance of the **Interface Protection** is to be demonstrated on site.

#### Note:

Within this Form A2-3 the term Power Park Module will be used but its meaning can be interpreted within Form A2-3 to mean Power Park Module, Generating Unit or Inverter as appropriate for the context. However, note that compliance must be demonstrated at the Power Park Module level.

If the Power Generating Module is Fully Type Tested and registered with the Energy Networks Association (ENA) Type Test Verification Report Register, the Installation Document (Form A3) should include the Manufacturer's reference number (the Product ID), and this form does not need to be submitted.

Where the Power Generating Module is not registered with the ENA Type Test Verification Report Register or is not Fully Type Tested this form (all or in parts as applicable) needs to be completed and provided to the DNO, to confirm that the Power Generating Module has been tested to satisfy all or part of the requirements of this EREC G99.

Manufacturer's reference number		Primo GEN24 4.0		
PGM technology		IGBT power modules, transformerless		
Manufacturer name		Fronius International GmbH		
Adress		Guenter Fronius Str.1 4600 Wels-Thalheim, Austria		
Tel	+43-7242-241-0		Fax	+43-7242-241-224
E:mail	pv@fronius.com		Web site	www.fronius.com



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Manufacturer compliance declaration. - I certify that all products supplied by the company with the above Type Tested Manufacturer's reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site Modifications are required to ensure that the product neets all the requirements of EREC G99.

Signed FRONTIS INTERNATIONAL GMBH On behalf of Gunter Frontisch Ladout Lester Indulterin

Tel: +43/(0) 72 42/241-0 Fax: 47 8 25

Note that testing can be done by the Manufacturer of an individual component or by an external test house.

Where parts of the testing are carried out by persons or organisations other than the **Manufacturer** then that person or organisation shall keep copies of all test records and results supplied to them to verify that the testing has been carried out by people with sufficient technical competency to carry out the tests.



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1. Operating Range: Five tests should be carried with the Power Generating Module operating at Registered Capacity and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within  $\pm$  5 % of the apparent power value set for the entire duration of each test sequence.

Frequency, voltage and **Active Power** measurements at the output terminals of the **Power Generating Module** shall be recorded every second. The tests will verify that the **Power Generating Module** can operate within the required ranges for the specified period of time.

The Interface Protection shall be disabled during the tests.

In case of a PV Power Park Module the PV primary source may be replaced by a DC source.

In case of a full converter **Power Park Module** (eg wind) the primary source and the prime mover **Inverter**/rectifier may be replaced by a DC source.

Test 1	
Voltage = 85% of nominal (195.5 V), Frequency = 47.0 Hz, Power factor = 1, Period of test 20 s	Always connected
Test 2  Voltage = 85% of nominal (195.5 V), Frequency = 47.5 Hz, Power factor = 1, Period of test 90 minutes	Always connected
Test 3  Voltage = 110% of nominal (253 V), Frequency = 51.5 Hz, Power factor = 1, Period of test 90 minutes	Always connected
Test 4  Voltage = 110% of nominal (253 V), Frequency = 52.0 Hz, Power factor = 1, Period of test 15 minutes	Always connected
Test 5 RoCoF withstand  Confirm that the <b>Power Generating Module</b> is capable of staying connected to the <b>Distribution Network</b> and operate at rates of change of frequency up to 1 Hzs <sup>-1</sup> as measured over a period of 500 ms. Note that this is not expected to be demonstrated on site.	Always connected
Remark: During the tests 1, 2, 3, 4 and 5 the unit do	oes not disconnect, tests have been passed.



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### 2. Power Quality - Harmonics:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12 The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 61000-3-12 for three phase equipment.

For **Power Generating Modules** of **Registered Capacity** of greater than 75 A per phase (ie 50 kW) the installation shall be designed in accordance with EREC G5.

Danner Cananatina	Mandada Anakad An	DC EN 04000 0 40
Power Generating	<b>Module</b> tested to	BS EN 61000-3-12

Power Generating Module rating per phase (rpp)		4.0 kVA		Harmonic % = Measured Value (A) x 23/rating per phase (kVA)		
Harmonic	At 45-55% of Capacity		100% of Regi Capacity		Limit in BS	EN 61000-3-12
	Measured Value MV in Amps	%	Measured Value MV in Amps	%	1 Phase	3 phase
2	0.015	0.088	0.012	0.067	8%	8%
3	0.020	0.113	0.024	0.141	21.6%	Not stated
4	0.016	0.091	0.009	0.050	4%	4%
5	0.015	0.084	0.018	0.105	10.7%	10.7%
6	0.009	0.053	0.005	0.031	2.67%	2.67%
7	0.015	0.089	0.006	0.033	7.2%	7.2%
8	0.008	0.043	0.005	0.029	2%	2%
9	0.076	0.435	0.033	0.187	3.8%	Not stated
10	0.007	0.038	0.005	0.030	1.6%	1.6%
11	0.049	0.284	0.026	0.147	3.1%	3.1%
12	0.006	0.034	0.005	0.029	1.33%	1.33%
13	0.032	0.182	0.023	0.130	2%	2%
THD <sup>17</sup>	-	3.28	-	0.53	23%	13%
PWHD <sup>18</sup>	-	1.33	-	2.08	23%	22%

<sup>&</sup>lt;sup>17</sup> THD = Total Harmonic Distortion

<sup>&</sup>lt;sup>18</sup> PWHD = Partial Weighted Harmonic Distortion



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#### 3. Power Quality - Voltage fluctuations and Flicker:

For **Power Generating Modules** of **Registered Capacity** of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance.

For Power Generating Modules of Registered Capacity of greater than 75 A per phase (ie 50 kW) the

installation shall be designed in accordance with EREC P28.

	Starting			Stopp	Stopping			Running		
	d <sub>max</sub>	d <sub>c</sub>	d <sub>(t)</sub>	d <sub>max</sub>	d <sub>c</sub>	d <sub>(t)</sub>	P <sub>st</sub>		P <sub>lt</sub> 2 h	ours
Measured Values at test impedance	1	0.95		1	0.9		0.29	91	0.273	4
Normalised to standard impedance	1	0.95	-	1	0.9		0.29	91	0.273	4
Normalised to required maximum impedance		-	-	-	-	-	-		-	
Limits set under BS EN 61000-3-11	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0		0.65	
Test Impedance	R		0.4	Ω		Х		0.25		Ω
Standard Impedance	R		0.24 *	Ω		Х		0.15 <sup>*</sup>		Ω
Maximum Impedance	R		-	Ω		X		-		Ω

<sup>\*</sup> Applies to three phase and split single phase Power Generating Modules.

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or above.

Normalised value = Measured value\*reference source resistance/measured source resistance at test point.

Single phase units reference source resistance is 0.4  $\Omega$ 

Two phase units in a three phase system reference source resistance is  $0.4 \Omega$ .

Two phase units in a split phase system reference source resistance is  $0.24~\Omega$ .

Three phase units reference source resistance is 0.24  $\Omega$ .

Where the power factor of the output is under 0.98 then the XI to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start	10:45	Test end	12:45	2020-10-20
Test location	· V	aboratories, Fronius Internation us Str 1, A-4600 Wels-Thalheim,	•	

<sup>^</sup> Applies to single phase **Power Generating Module** and **Power Generating Modules** using two phases on a three phase system.



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4. Power quality - DC injection: The tests should be carried out on a single Generating Unit. Tests are to be carried out at three defined power levels ±5%. At 230 V a 50 kW three phase Inverter has a current output of 217 A so DC limit is 543 mA. These tests should be undertaken in accordance with Annex A.7.1.4.4. Test power level 100% 10% 55% Recorded value in 0.0130 0.0085 0.0071 **Amps** as % of rated AC 0.0747 0.0488 0.0408 current Limit 0.25% 0.25% 0.25%

**5. Power Factor:** The tests should be carried out on a single **Power Generating Module**. Tests are to be carried out at three voltage levels and at **Registered Capacity**. Voltage to be maintained within ±1.5% of the stated level during the test. These tests should be undertaken in accordance with Annex A.7.1.4.2.

Voltage	0.94 pu (216.2 V)	1 pu (230 V)	1.1 pu (253 V)
Measured value	1.000	1.000	1.000
Power Factor Limit	>0.95	>0.95	>0.95

6. Protection - Frequency tests: These tests should be carried out in accordance with Annex A.7.1.2.3. Setting Trip test "No trip tests" **Function** Frequency Time Frequency Time Frequency Confirm no trip delay delay /time U/F stage 1 20s 47.501Hz 20.058s 47.7Hz 47.5Hz No trip occurred 30s U/F stage 2 47Hz 0.5s47.00Hz 0.56s 47.2Hz No trip occurred 19.5s 46.8Hz No trip occurred 0.45sO/F stage 1 0.5s 52Hz 52.003Hz 0.56s 51.8Hz No trip occurred 120.0s 52.2Hz No trip occurred

Note. For frequency trip tests the frequency required to trip is the setting  $\pm$  0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The "No trip tests" need to be carried out at the setting  $\pm$  0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

0.45s

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Function	Setting		Trip test		"No trip tes	its"
	Voltage	Time delay	Voltage	Time delay	Voltage /time	Confirm no trip
U/V	0.8 pu (184V)	2.5s	182.69V	2.54s	188V 5.0s	No trip occurred
Hillmon	A TENEDA				180V 2.45s	No trip occurred
O/V stage 1	1.14 (262.2V)	1.0s	262.86V	1.043s	258.2V 5.0s	No trip occurred
O/V stage 2	1.19 (273.7V)	0.5s	275.53V	0.537s	269.7V 0.95s	No trip occurred
					277.7V 0.45s	No trip occurred

Note for Voltage tests the Voltage required to trip is the setting  $\pm 3.45$  V. The time delay can be measured at a larger deviation than the minimum required to operate the protection. The No trip tests need to be carried out at the setting  $\pm 4$  V and for the relevant times as shown in the table above to ensure that the protection will not trip in error.

8. Protection – 62116. Annex A.7.		ns test: Thes	e tests should	be carried ou	it in accordance	ce with BS EN
The following subs	set of tests sho	uld be recorde	d in the followi	ng table.		
Test Power and	33%	66%	100%	33%	66%	100%
imbalance	-5% Q	-5% Q	-5% P	+5% Q	+5% Q	+5% P
	Test 22	Test 12	Test 5	Test 31	Test 21	Test 10
Trip time	242.0 ms	238.0 ms	315.8 ms	230.6 ms	228.0 ms	261.8 ms
Limit is 0.5s						

Loss of Mains Protection, Vector Shift Stability test. This test should be carried out in accordance with Annex A.7.1.2.6.						
	Start Frequency	Change	Confirm no trip			
Positive Vector	49.5Hz	+50 degrees	No trip occurred			
Negative Vector	50.5Hz	-50 degrees	No trip occurred			

Loss of Mains Protection, RoCoF Stability test: This test should be carried out in accordance with Annex A.7.1.2.6.							
Ramp range Test frequency ramp: Test Duration Confirm no trip							
49.0 Hz to 51.0Hz	+0.95 Hzs <sup>-1</sup>	2.1 s	No trip occurred				
51.0 Hz to 49.0Hz	-0.95 Hzs <sup>-1</sup>	2.1 s	No trip occurred				



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9. Limited Frequency S using the specific threshold. This test should be carried or	frequency of 50.4 h	Hz and <b>Droop</b> o	f 10%.	d be carried out	
Active Power response to r frequency injection tests are					
Alternatively, simulation resu	ılts should be note	d below:			
Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient	
Step a) 50.00 Hz ±0.01 Hz	4013W	50.00Hz	=	V	
Step b) 50.45 Hz ±0.05 Hz	3975W	50.45Hz			
Step c) 50.70 Hz ±0.10 Hz	3773W	50.70Hz			
Step d) 51.15 Hz ±0.05 Hz	3412W	51.15Hz	4.1kW 20%/		
Step e) 50.70 Hz ±0.10 Hz	3773W	50.70Hz	7		
Step f) 50.45 Hz ±0.05 Hz	3975W	50.45Hz			
Step g) 50.00 Hz ±0.01 Hz	4013W	50.00Hz			
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient	
Step a) 50.00 Hz ±0.01 Hz	2007W	50.00Hz			
Step b) 50.45 Hz ±0.05 Hz	1988W	50.45Hz			
Step c) 50.70 Hz ±0.10 Hz	1887W	50.70Hz			
Step d) 51.15 Hz ±0.05 Hz	1707W	51.15Hz	2.1kW	20%/Hz	
Step e) 50.70 Hz ±0.10 Hz	1885W	50.70Hz			
Step f) 50.45 Hz ±0.05 Hz	1987W	50.45Hz			
Step g) 50.00 Hz ±0.01 Hz	2007W	50.00Hz			

10. Protecti	ion - Re-conne	ction timer.				
	prove that the re requency to with			after a minimum le 10.1.	delay of 20 s fo	r restoration of
Time delay setting	Measured delay			reconnection what outside stage 1		
20.0s	47.3s		At 1.16 pu (266.2V)	At 0.78 pu (180.0V)	At 47.4Hz	At 52.1Hz
	that the <b>Power</b> on the re-connect.	Generation	No re- connect occurred	No re- connect occurred	No re- connect occurred	No re- connect occurred



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For <b>inverter</b> output				
Time after fault	Volts	Amps		
20 ms	15.1	34.6		
100 ms	15.1	23.3		
250 ms	15.1	15.7		
500 ms	15.1	11.3		
Time to trip	0.145	In seconds		
	value below 50 volts with	e on the output side of the switching in 0.5 s.	NA 	
device is reduced to a		in 0.5 s.	NA .	
device is reduced to a  13. Wiring function  Confirm that the relev	value below 50 volts within	in 0.5 s.	NA	
13. Wiring function Confirm that the relevof commissioning)	value below 50 volts within all tests: If required by parant test schedule is attack	ara 15.2.1.		
13. Wiring function Confirm that the relevof commissioning)  14. Logic interface	value below 50 volts withing all tests: If required by parant test schedule is attack.  (input port).	ara 15.2.1. hed (tests to be undertaken at time		
13. Wiring function Confirm that the relevof commissioning)  14. Logic interface	value below 50 volts withing all tests: If required by parant test schedule is attack.  (input port).	ara 15.2.1.		
13. Wiring function Confirm that the relevof commissioning)  14. Logic interface	value below 50 volts withing all tests: If required by parant test schedule is attack.  (input port).  port is provided and can be	ara 15.2.1. hed (tests to be undertaken at time	NA	

